

PATENT TESSERA 3.3-018 CONT CONT II CONT II CONT II

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of Khandros, et al.

Group Art Unit 2815

Appln. No. 09/691,807

Examiner: Sheila Clark

Filed: October 19, 2000

Confirmation No. 5389

For: STACKED CHIP ASSEMBLY

Dated: July 10, 2002

Commissioner For Patents Washington, D.C. 20231

TRANSMITTAL OF FORMAL DRAWINGS

X

Sir:

Enclosed herewith are twenty (20) sheets of formal drawings for filing in connection with the above-identified application. The formal drawings are submitted in order to correct the informalities listed in form PTO-948.

In the event there are any fees due and owing in connection with this matter, please charge same to our Deposit Account No. 12-1095.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on July 10, 2002.

(Signature) Arnold H. Krumholz

Typed or Printed Name of Person Signing Certificate

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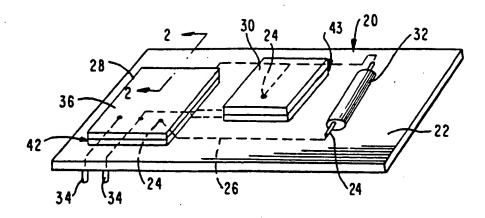
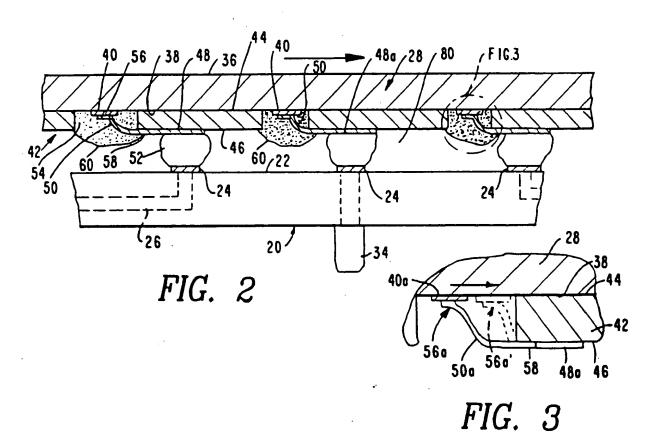


FIG. 1



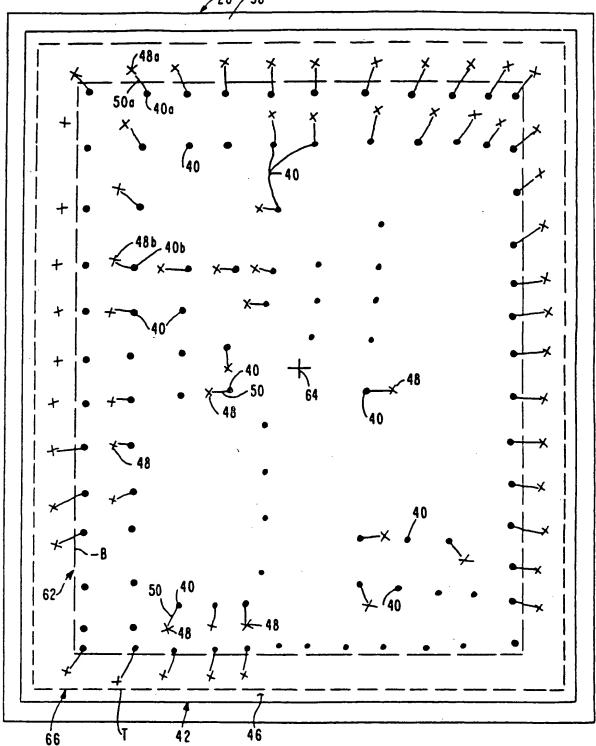


FIG. 4

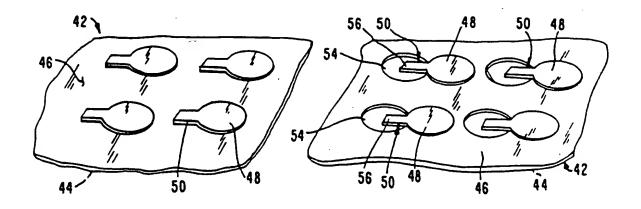


FIG. 5A

FIG. 5B

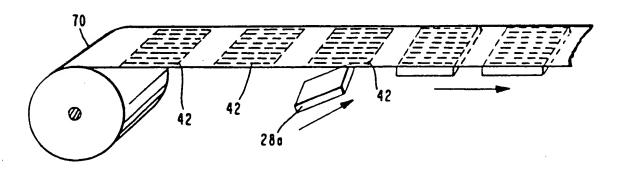
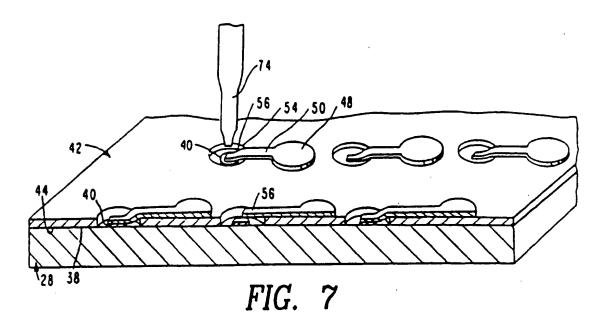


FIG. 6



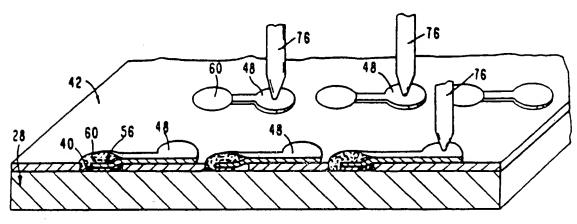
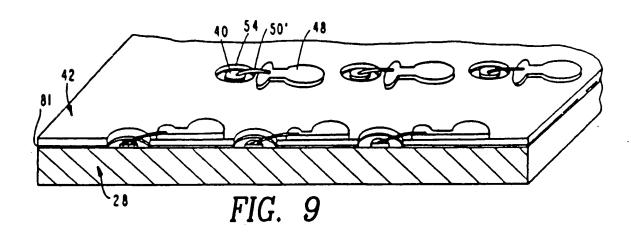
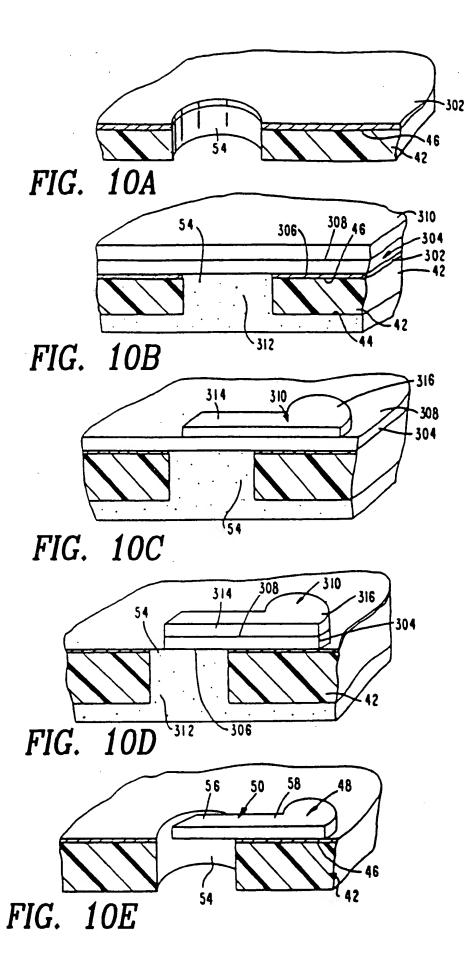


FIG. 8





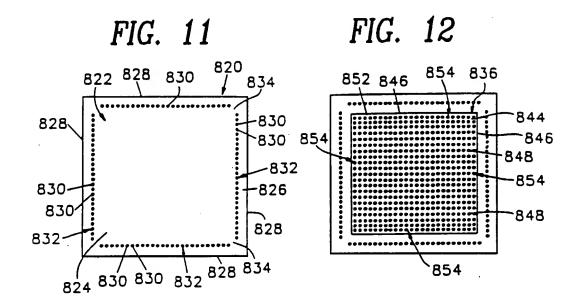


FIG. 17

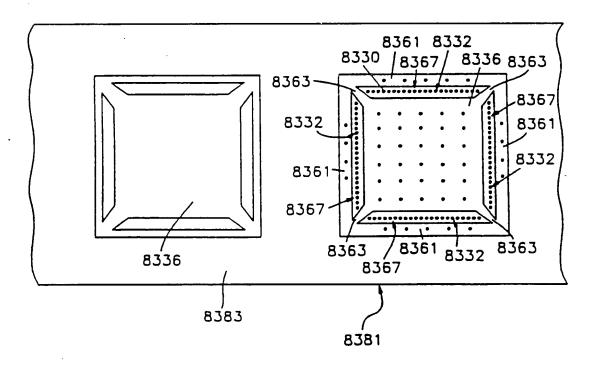
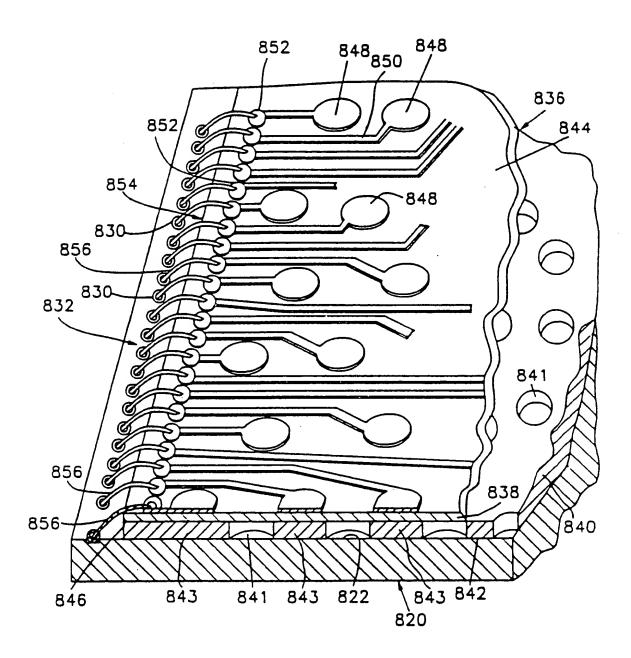
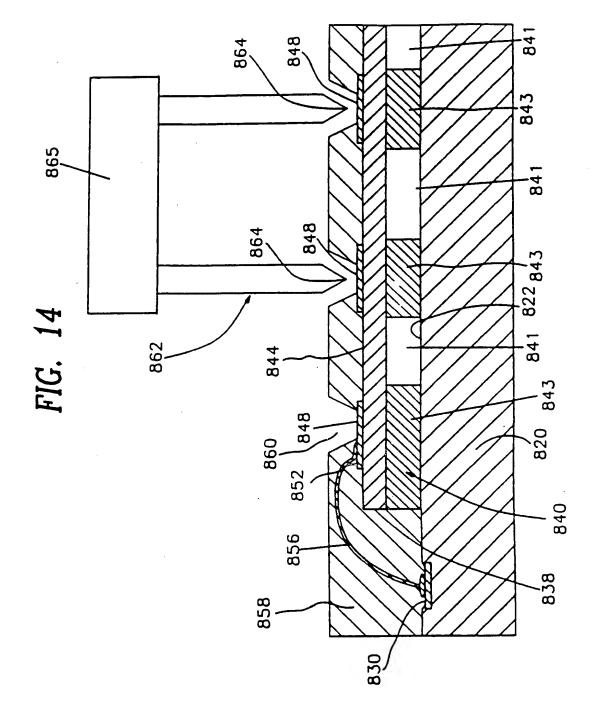
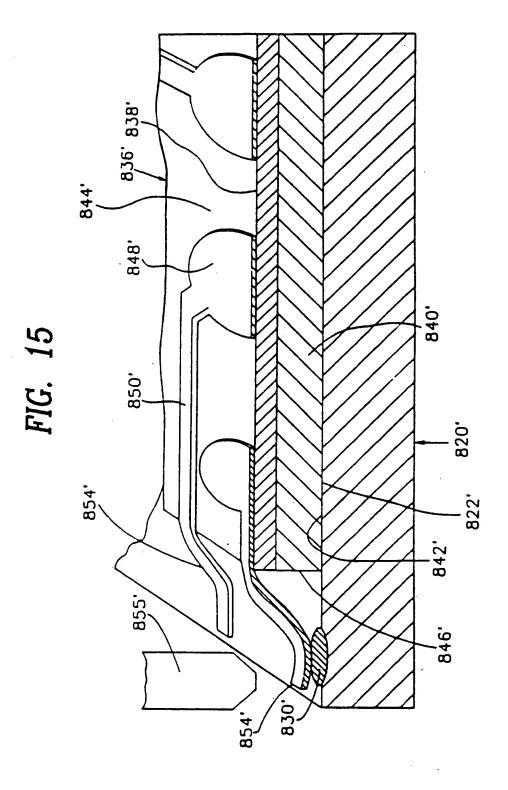


FIG. 13







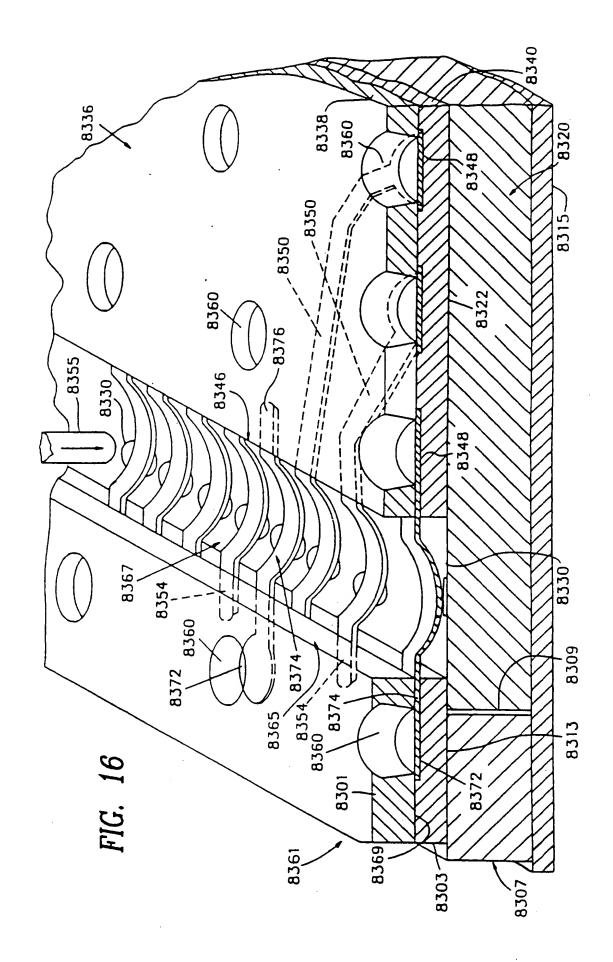


FIG. 18

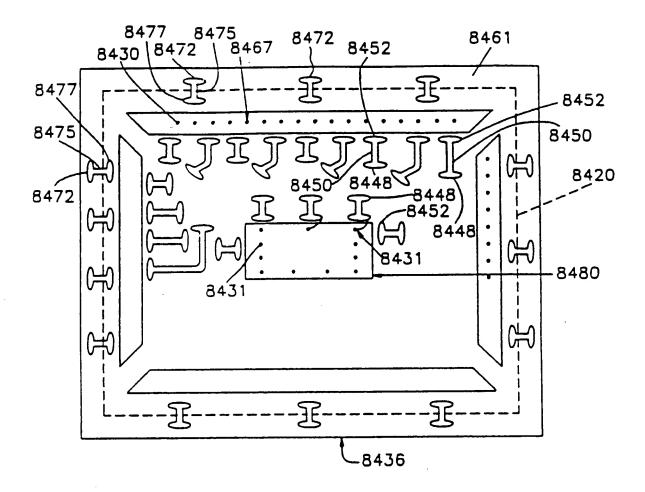
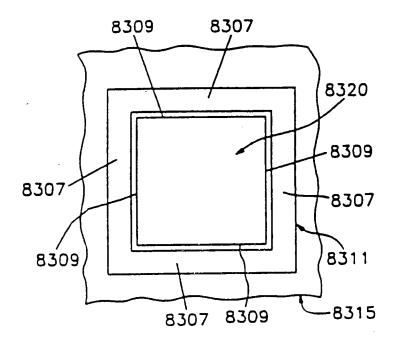


FIG. 19



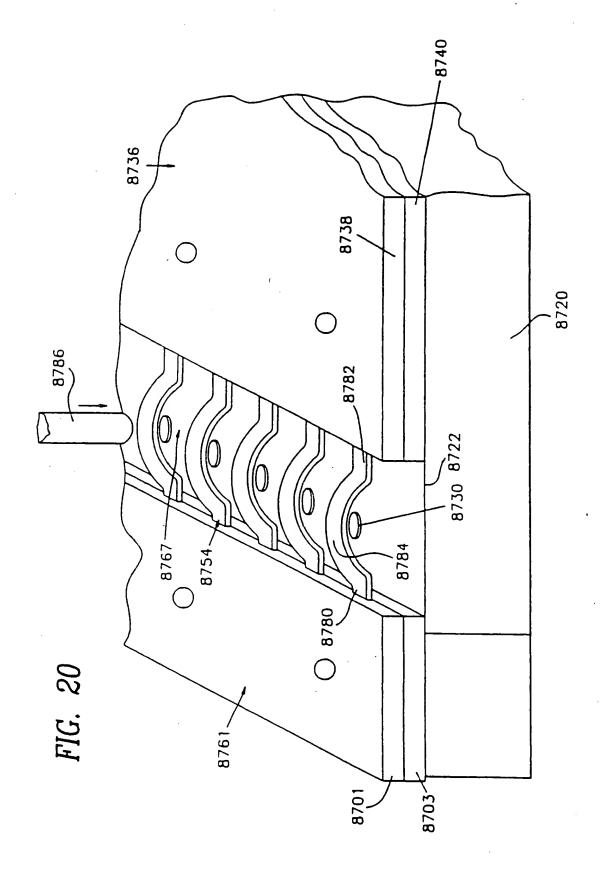


FIG. 21

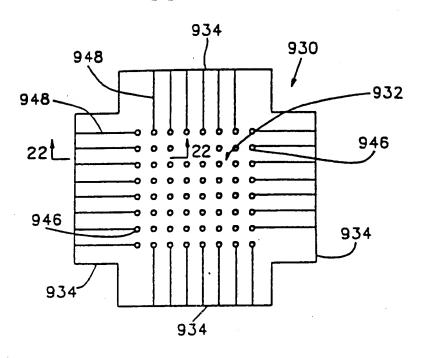


FIG. 22

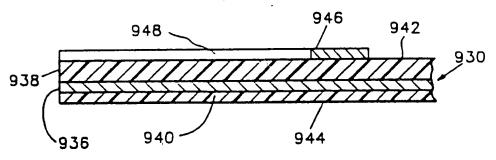


FIG. 23

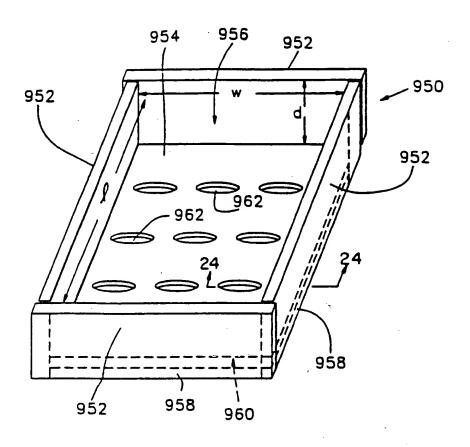


FIG. 24

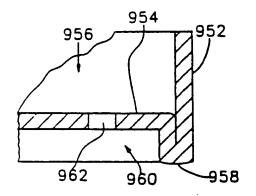


FIG. 25

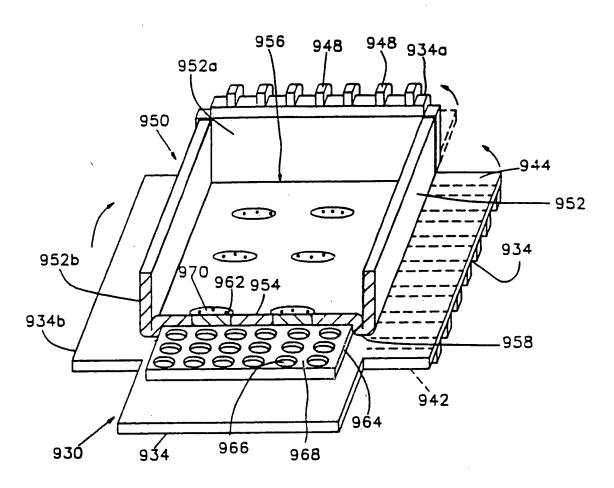
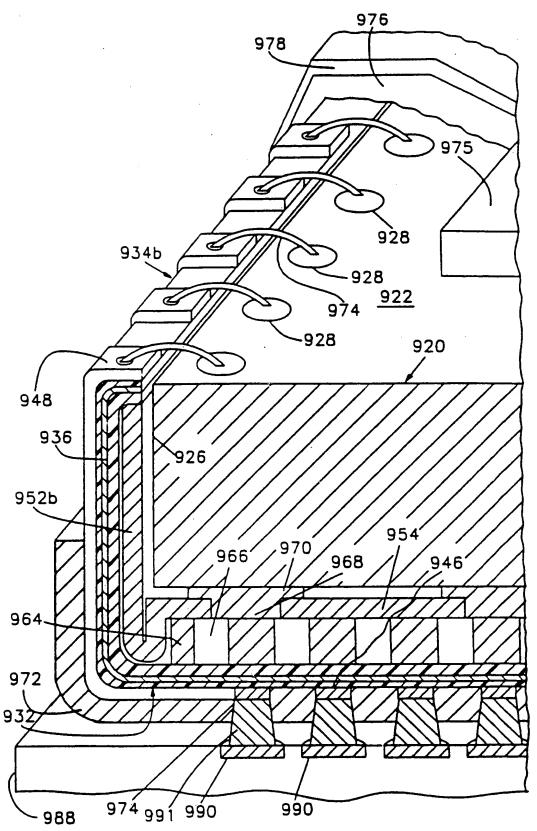


FIG. 26



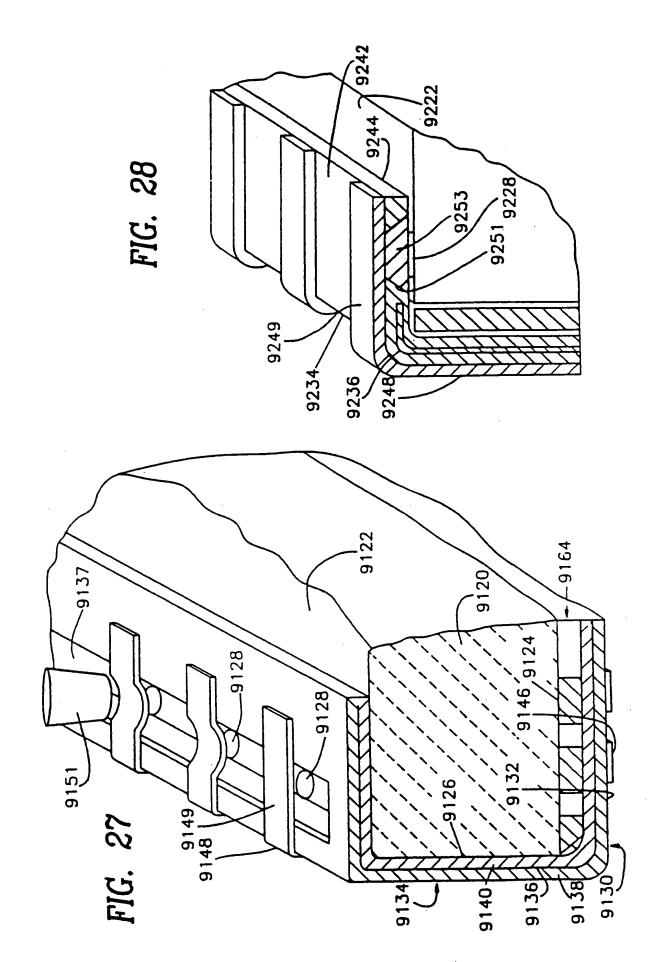


FIG. 29

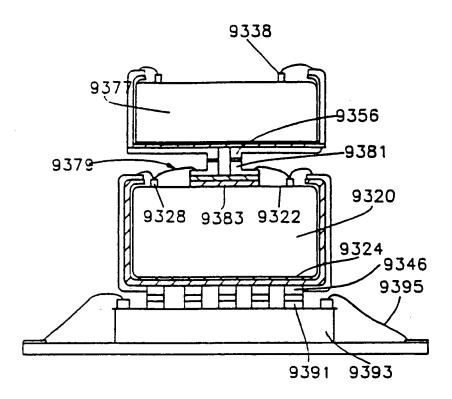
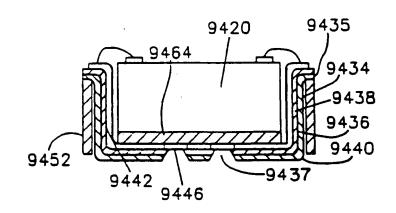
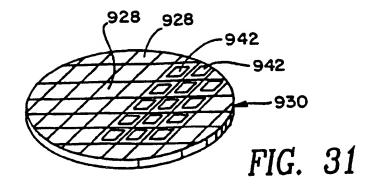
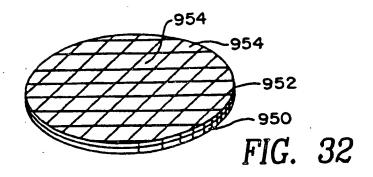


FIG. 30







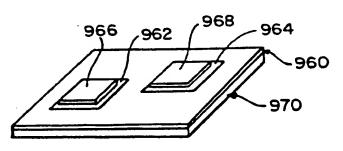


FIG. 33